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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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| APPL NUM 10077779 | FILING DATE 02/20/2002 | CLASS 053 | SUBCLASS -2 | GAU 3721 | EXAMINER D. E. Nguyen |
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**APPLICANTS: Sasakura Takahiro; Abe Seiichi;

**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED

JAPAN 2001-275887 09/12/2001

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| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> | ATTORNEY DOCKET NO |
| Foreign priority claimed 35 USC 119 conditions met | | <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no | 501.41215X00 |
| Verified and Acknowledged Examiners's initials | | | |
| TITLE : Semiconductor integrated circuit modules, manufacturing methods and usage thereof | | | |

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)

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|----------------------------|-----------|--|----------------|----------------------|
| NOTICE OF ALLOWANCE MAILED | | Assistant Examiner | CLAIMS ALLOWED | |
| | | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | DRAWING | | |
| Amount Due | Date Paid | Sheets Drwg. | Figs.Drwg. | Print Fig. |
| TERMINAL | | Primary Examiner | | Application Examiner |
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